UC4G_Shanghai_Workshop

Ongoing MIMO Wireless Testbed Development in Shandong University

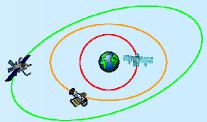
Presenter: Dr. Jian Sun

Tel: 13685416980 URL: http://202.194.20.8

Wireless Mobile Communication and Transmission (WMCT) Lab. Shandong University









Introduction and Motivation

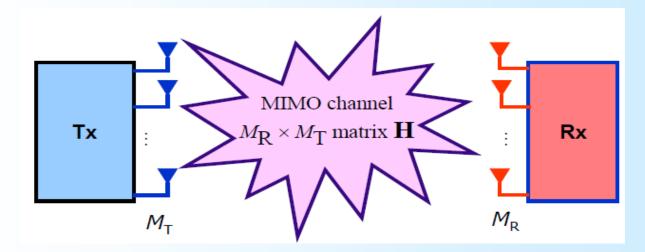
- Non-real Time MIMO Testbed
- Non-real Time MIMO Testbed
- Future work



Introduction and Motivation(1)

• MIMO-Multiple Input Multiple Output:

- Exploits the spatial dimension of mobile propagation channels.
- Main benefits: spatial multiplexing gain and spatial diversity gain
- Widely used in wireless communication standards: 3GPP LTE, Wi-Fi (802.11n), WiMAX(802.16d), mobile WiMAX(802.16e), etc.





Introduction and Motivation(2)

- Verification Algorithm of MIMO Techniques
 - Computer Simulation
 - Easy, low cost
 - Simulated channel model, (3GPP SCM(E), WINNER II, TGn), maybe not coincide with the true environment
 - MIMO Testbed
 - Complex, higher cost
 - True physical channel





Introduction and Motivation(3)

• MIMO Testbed

- Non-real-time testbed
 - non-real-time (offline) signal processing by PC software
 - Univ. of Vienna (ICS based, 2005) ,University of Texas(Hydra,2005)
- Real-time testbed
 - Real time signal processing by DSP & FPGA
 - ETH Zurich(2004-),NTT DoCoMo(2004-), Siemens AG 和 Heinrich Hertz(2004-),), University of South Australia(2006), Univ. of Rice(WARP,2006), Univ. of Duisburg(Sundance,2008)
 - SEU: Beyond 3G system (Gao Xiqi,2006), Gbps Wireless Communication Testbed (Wang Xiangyang, 2008)



Progress on MIMO Testbed Development in SDU

Non-real Time System

2006	5.3 200	6.10 2	2007.6	
System Scheme	Equipment Purchase	System Completene		ontinuous h(802.11n,etc)

Real Time System

200	6.9 200	08.1 2009	9.5 2	009.10	2009.12	
System Scheme	Equipment Purchase	SISO Video Transmission	OFDM System	2x2 MIMO-OFDM System		
	Only one channel DAC supported by current hardwa so still stay at SysGen simulation stage					
					(AN)	





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Photographs of non-real time system

Transmitter System





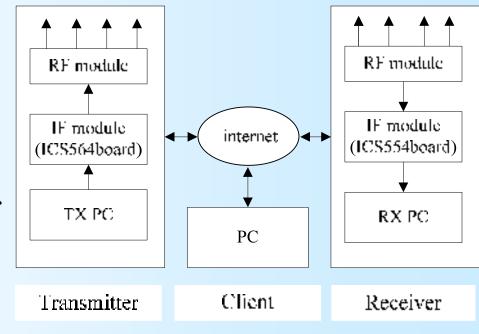




Non-real Time MIMO Testbed

General Description

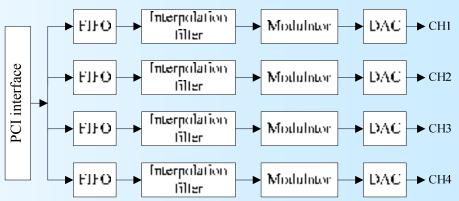
- Linear Antennas Array
- Transmitter/ Receiver
 RF converter
- Transmitter PC/Receiver
 PC: DUC /DAC,
 ADC/DDC
- Client PC: Baseband
 Signal Processing (data generation and recovery)



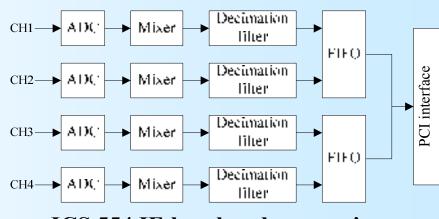


Intermediate Frequency (IF) Processing





ICS 564 baseband-IF conversion



ICS 554 IF-baseband conversion



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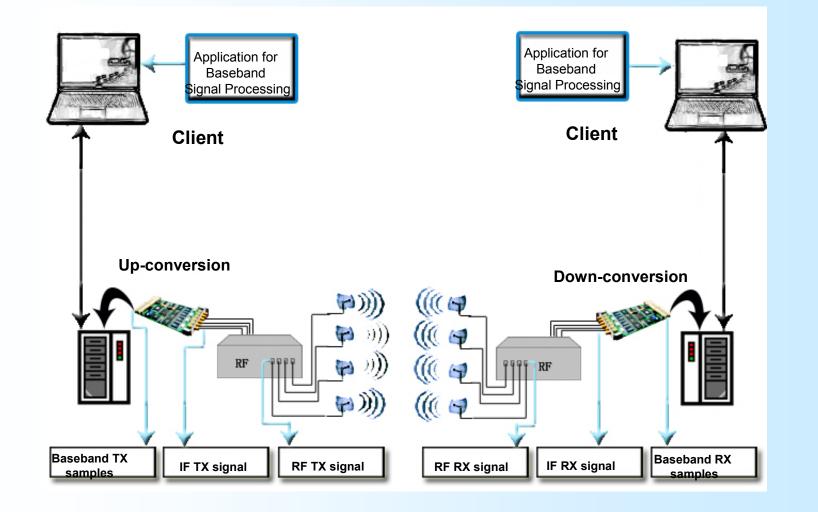
Radio Frequency / IF converter

Parameters

- RF frequency : 2453MHz
- Intermediate Frequency: 70MHz
- Number of Channel: 4
- BW: ≤ 20 MHz(for RF), 10MHz(for BB)
- Frequency stability of LO: $\triangle f/f \leq \pm 2ppm$
- Phase Noise of LO: $\Phi < -85 dBc@10 KHz$
- Output IM3: -35dBc
- Power per Antenna: 20dBm
- Isolation between channels: 40dB
- Noise Figure: <4.5dB
- Receiving Level: -80dBm ~ -30 dBm
- Antenna Gain: 6dBi omni-directional
- Space between antennas: Adjustable

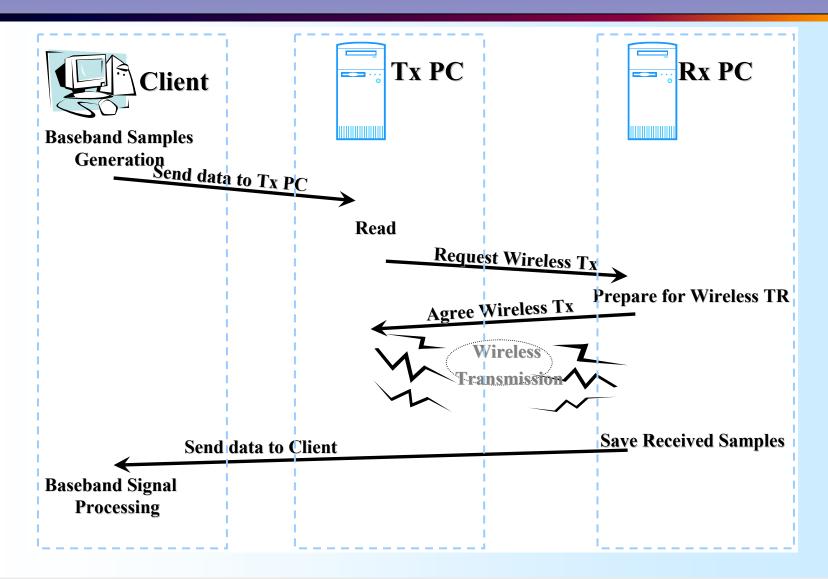


System Architecture

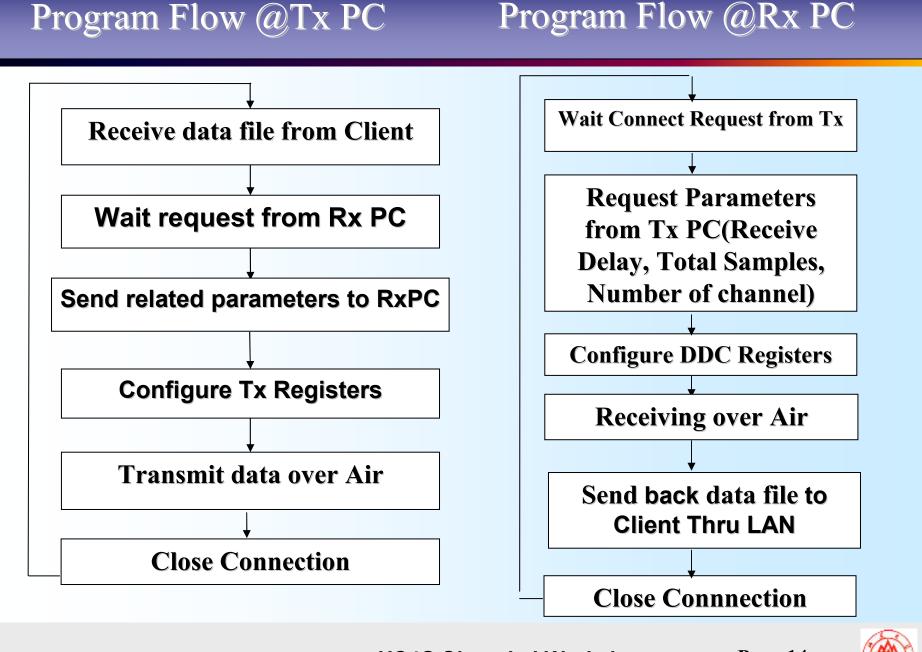




Data Transfer Flow





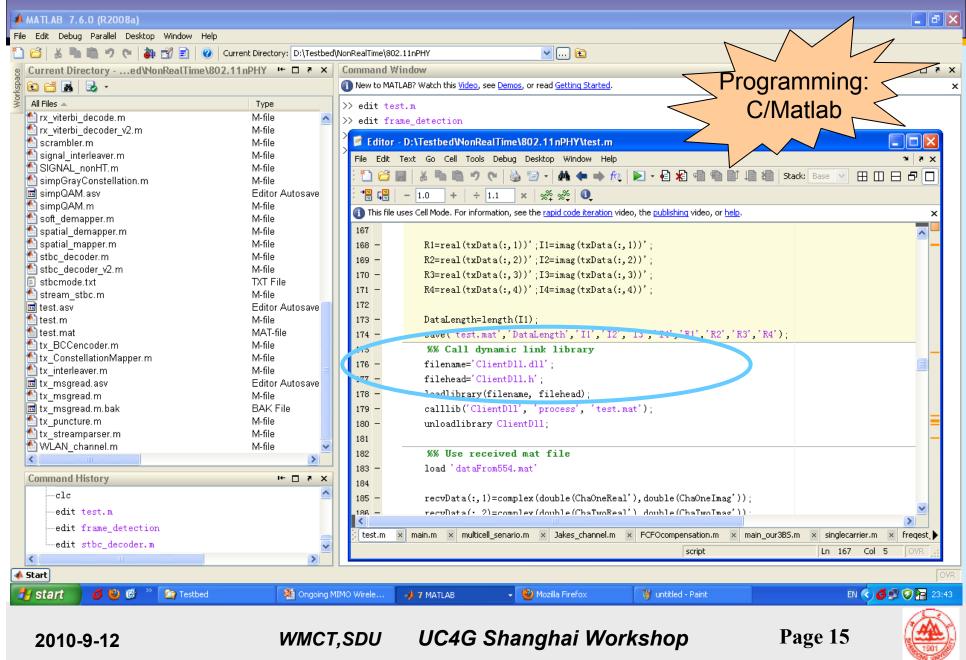


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Software Programming @ Client



Research on Non-real Time Platform

- link-level Test, simplex transmission
 - 802.11n System ∇ GI IF and ► IDFT D/A ┢ insertion \mathbf{R}^{F} Constellation mapping γ IF and GI Spatial mapping Pilot insertion CSD 🔶 ▶ IDFT ► D/A -► insertion $\mathbf{R}\mathbf{F}$ STBC γ GI IF and ► CSD -► D/A -► IDFT → insertion $\mathbf{R}\mathbf{F}$ γ GI IF and CSD -► IDFT D/A • ► insertion $\mathbf{R}\mathbf{F}$

• WiMAX, etc



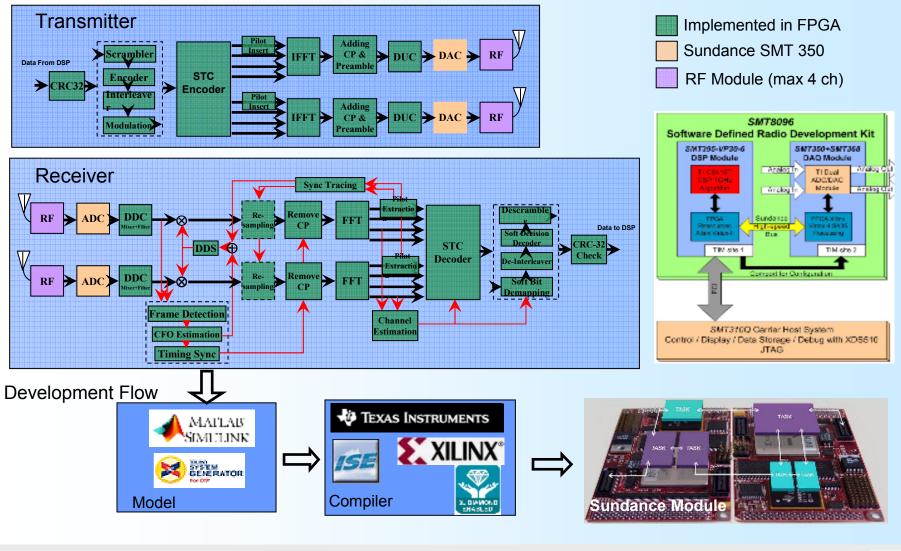
Outline

- Introduction and Motivation
- Non-real Time MIMO Testbed
- Real Time MIMO Testbed
- Future work



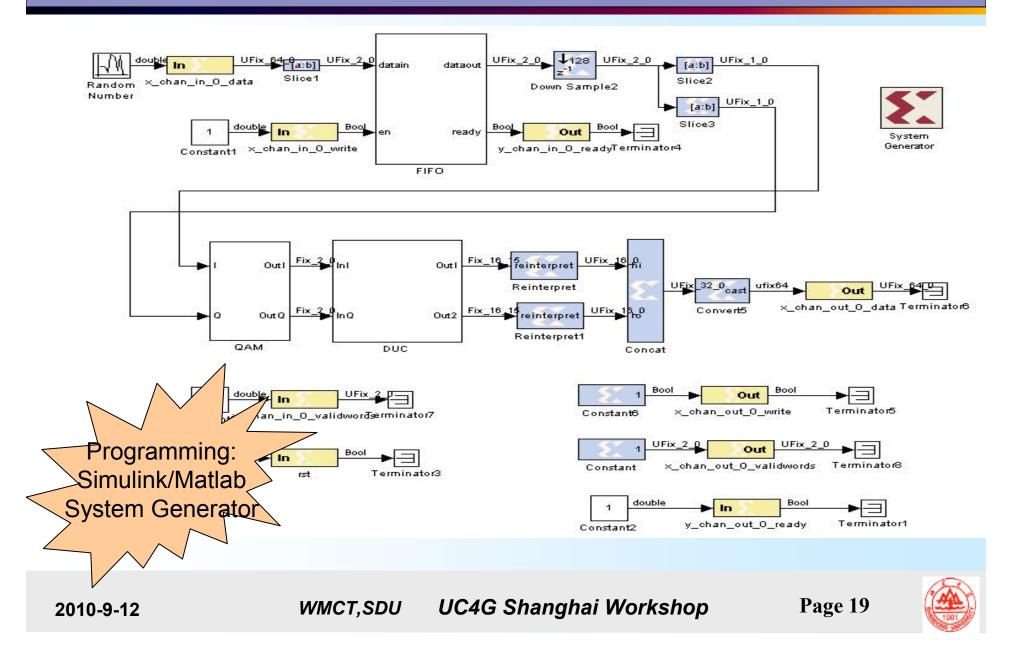


Implementation of MIMO-OFDM System



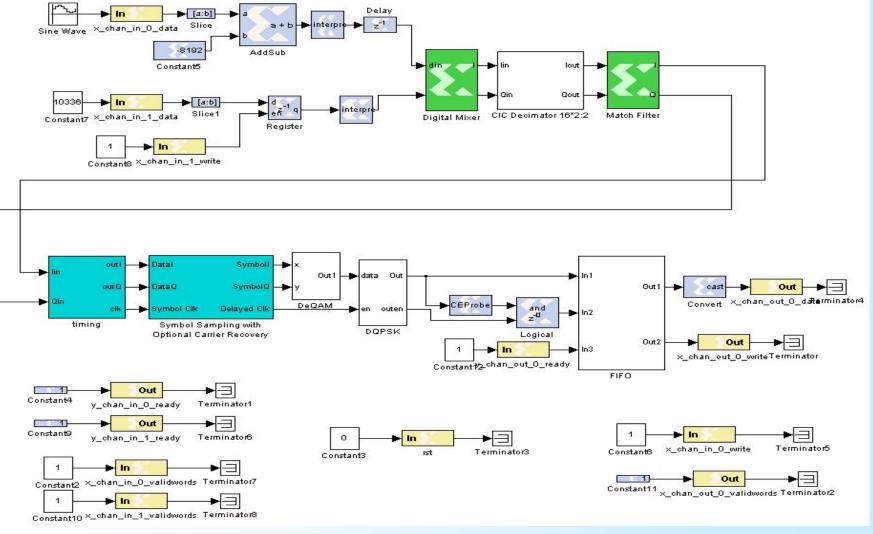


SISO Transmitter



SISO Receiver





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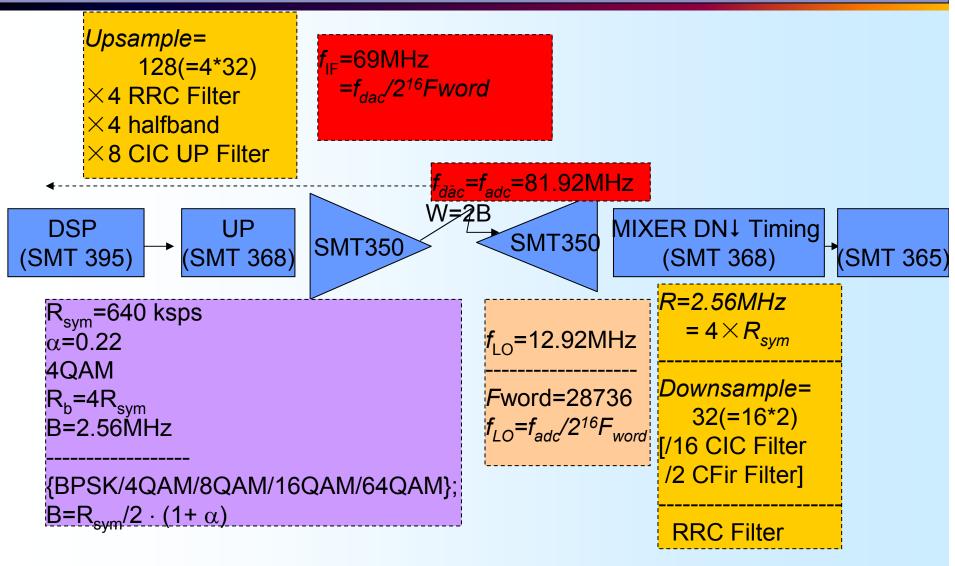
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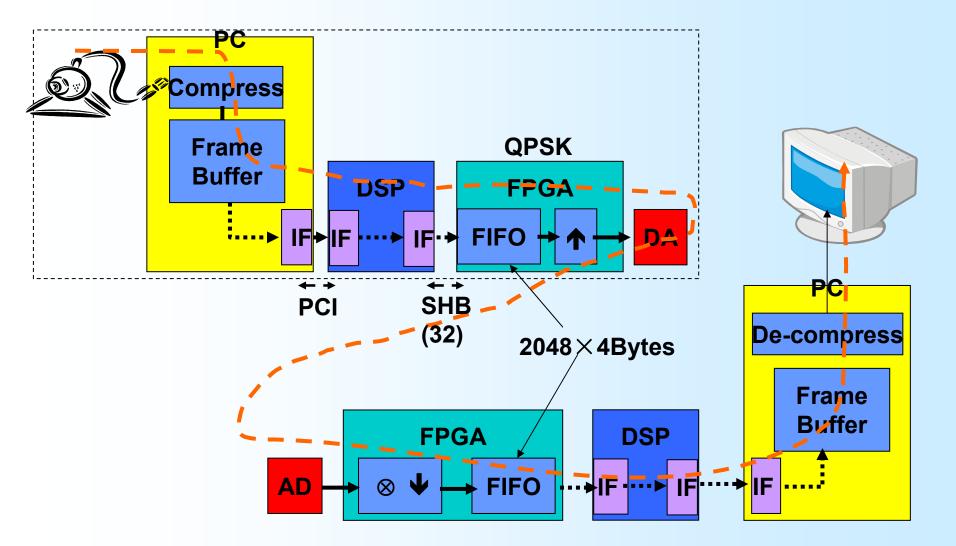


SISO--parameters





Video Transmission -- Data Flow

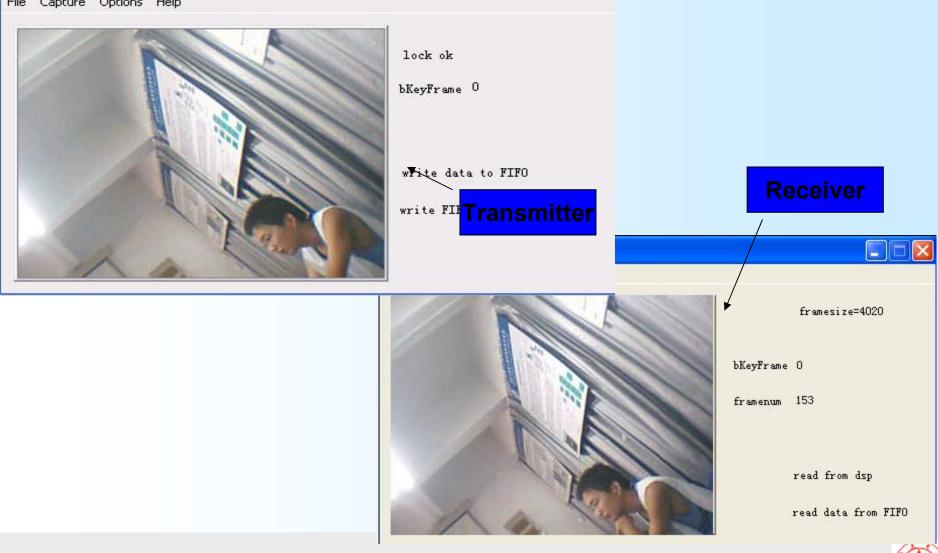




Video transmission

USBVIDEO

File Capture Options Help

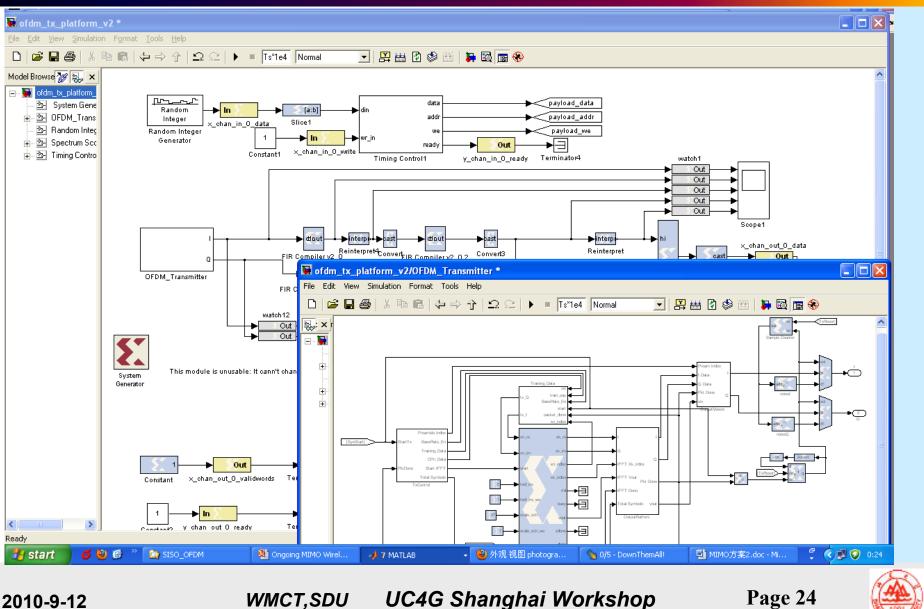


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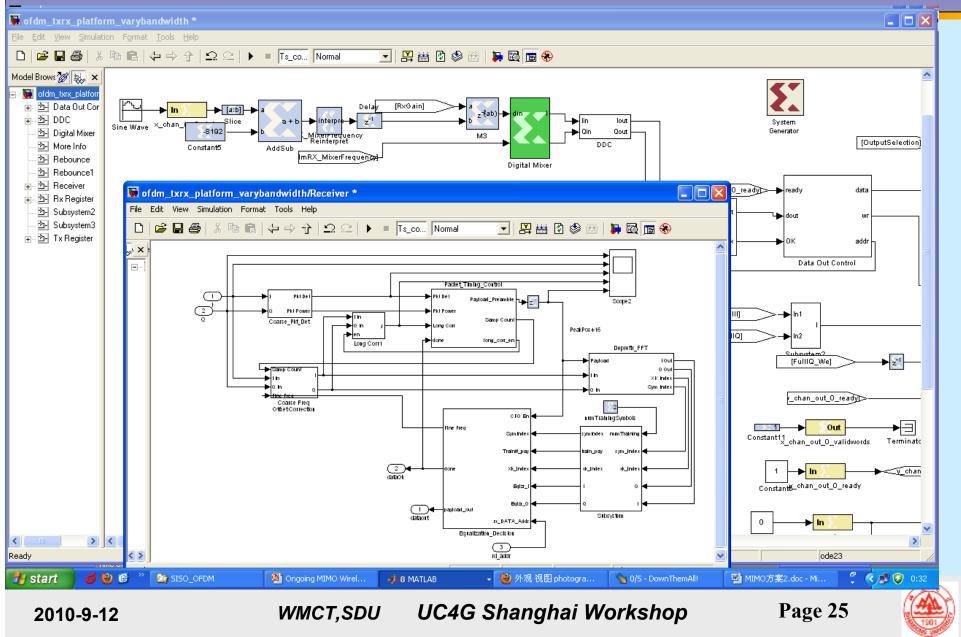
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OFDM Transmitter (refer to WARP)



OFDM Receiver (refer to WARP)



Processing Simulink/System Generator

🙀 ofdm_txrx_mimo *	System Generator: ofdm_txrx_mimo
File Edit View Simulation Format Tools Help	- Compilation Options
🗋 🖆 🛃 🎒 👗 ங 💼 🗇 수 수 🗅 으ୁ 🖂 🕨 = 7e3 🛛 Normal 💽 🛃 🛗 🕸 🎬 🐌 🔯 🌆	Compilation :
Madal Damara Red Ext. and	De HDL Netlist
ofdm_txrx_mimo/OFDM Tx MIMO *	Part: Pert
Elle Edit View Simulation Format Tools Help	Virtex2P xc2vp70-6ff1517
□ 🗳 🖬 🚭 👗 🖻 💼 🗘 🕁 🎲 🗠 🗠 🕨 = 7e3 Normal 💽 🔛 🚟 🕲 🍪 🕮 🖡 🗟 📠	Target directory :
Model Browser 🦉 😓 🗙	JTxRx_netlist Browse
- 😹 ofdm_txrx_mimo	Synthesis tool : Hardware description language :
Experimentation Training_Data Training_Data	XST VHDL V
⊡	Create testbench Import as configurable subsystem
Baserate_En Baserate_En FFT En Start Start	Clocking Options
double tx_1 xn_index	FPGA clock period (ns): Clock pin location :
xn_index	Multirate implementation : DCM input clock period (ns) : Clock Enables
Start Bool Vout Vout Vout Autor double	Provide clock enable clear pin
FFT En double AntA_Q double AntA_Q double Fix_14_13	Generator
Training_Data Bool	Override with doubles : According to Block Settings
Stat (FT double Stat	Simulink system period (sec) : 1
ontrol	Block icon display:
IFFT OutputBuffers	Block icon display: Default
	Generate OK Apply Cancel Help
	AntA_Q
	AntB_I
	Tx Scope 🗸 OFDM RX MIMO
Ready 106% Ready	FixedStepDiscrete
🛃 Start 🛛 🥝 🕲 🚱 🔌 😭 doc 🛛 🖓 Ongoing MIMO Wirel 🥠 8 MATLAB 🗾 🕹 流程图	chart - Goog 💊 0/5 - DownThemAll! 🕎 Microsoft Word 🛛 EN 🔇 🧭 🧭 0:02
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Outline

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Future Work

- Non-real Time System
 - -Faster and easier development of new system.
 - -Non-real Time Signal Processing
 - ICS554+ICS564 Cost about 200,000RMB (~28,600\$)
 - Cheaper Solution: USRP (Universal Software Radio Peripheral), 2 USRP1s and 2 USRP2s for Cognitive Radio testing are ordering from Ettus.



Future Work

- Real Time System
 - Real Time signal Processing
 - Complexity: more complex than C/Matlab, cimpler than VHDL/Verilog
 - Cost of Sundance SMT8036/SMT8096 cost also about 200,000 RMB
 - Cheaper Solution: System Design by reference to WARP Project.
 - Higher deployment complexity of new algorithm
 - Expected solution??? :MPP-DSP (proposal of National S&T Major Project in 2011)



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Thank You & Welcome to visit Shandong University

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